

## **EE 3446-001** **Circuit Analysis II**

Fall 2012

Dr. Howard T. Russell, Jr.  
Office: Nedderman Hall 526  
(817) 272-3154  
[hrussell@uta.edu](mailto:hrussell@uta.edu)

### **Catalog Course Description:**

**EE 3446 CIRCUIT ANALYSIS II** (3-3) Analysis and design of filters, oscillators, feedback configurations, and operational amplifiers. Dependent sources, device models, two-port networks, and mutual inductance and transformers. Network response functions, poles and zeros, network theorems, resonance, and the analysis and design of active filters. Application of phasors in steady-state circuit analysis. Introduction to distributed networks and transmission lines. Introduction to single-phase and three-phase balanced and unbalanced power networks, complex power, power factor correction, and maximum power transfer. Concurrent laboratory experiments complement lecture topics. Prerequisite: Grade C or better in both EE 2347 and EE 2415.

### **Topics:**

This course is a continuation of the material covered in EE 2415. Specific topics include the following:

- the Laplace transform –  
definition and applications  
transform circuit components
- s-domain network analysis matrix equations –  
algorithms for formulation  
matrix solution methods and Cramer's rule
- frequency-domain response functions –  
immittance and transfer functions  
poles and zeros
- network theorems –  
Tellegen's, superposition, reciprocity, Thevenin's and Norton's, substitution
- two-port networks –  
two-port parameters and formulation  
properties
- applications of two-port models –  
feedback amplifier analysis and design
- op-amps –  
parameters and models
- active RC filters –  
filter specifications  
design methods
- sinusoidal steady-state –  
steady-state networks  
phasors  
complex power  
three-phase networks.

### **Prerequisites:**

EE 2415, EE 2347.

**Textbooks:**

1. R.C. Dorf and J.A. Svoboda, *Introduction to Electric Circuits, 8<sup>th</sup> Edition*, John Wiley & Sons, Inc., 2010, ISBN 978-0-470-52157-1.
2. H.T. Russell, Jr., *EE 3446 Circuits II Lab Manual, V1.2*, OPAL<sub>tx</sub>, Fort Worth, TX, 2010.

**Tools:**

1. Scientific calculator. Calculators as components in communication devices are not suggested nor allowed on exams.
2. Breadboard (mandatory).
3. Toolbox (mandatory) – containing an assortment of electronics tools consisting of needle nose pliers, diagonal cutters, tweezers, precision knife set, pocket screwdrivers.
4. Acrobat 6.0 Viewer by Adobe (<http://www.adobe.com>)
5. A good web browser.

**Times and Locations:**

*Section 001 Lecture* – Monday, Wednesday, and Friday, 10:00 am to 10:50 am, NH106.

*Section 101 Lab* – Monday, 1:00 pm to 3:50 pm, NH129A.

*Section 102 Lab* – Wednesday, 1:00 pm to 3:50 pm, NH129A.

**GTAs:**

*Lecture and lab:* TBD.

| EE 3446-001<br>Lecture Schedule |      |                                                                                                                                                                                                |                                               |
|---------------------------------|------|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-----------------------------------------------|
| Week/Date                       |      | Lecture Topics                                                                                                                                                                                 | Reading Assignment                            |
| 1                               | 8-24 | Course introduction and set-up.                                                                                                                                                                |                                               |
| 2                               | 8-27 | Review of time-domain ODE for RLC networks; describing equation, roots of the characteristic polynomial. Laplace transform; definition, linearity, transforms of simple time-domain functions. | Chapter 14 – 14.1 to 14.4.                    |
| 3                               | 9-3  | Transformed circuit components; applications on RLC networks. Formulation of the mesh-analysis matrix equation (MAME); solution methods.                                                       | Chapter 14 – 14.5 to 14.7; handouts.          |
| 4                               | 9-10 | Formulation of the node-analysis matrix equation (NAME); solution methods. Inverse Laplace transformation; PFE methods.                                                                        | Chapter 14 – 14.4 and 14.11; handouts.        |
| 5                               | 9-17 | s-domain network response functions; DP immittance and transfer functions; poles and zeros. Frequency-domain response; magnitude, phase and group delay; Bode plots; complex s-plane plots.    | Chapter 14 – 14.8; Chapter 13 – 13.1 to 13.4. |
| 6                               | 9-24 | Stability using s-domain models; significance of poles and zeros.                                                                                                                              | Chapter 14 – 14.10.                           |
| 7                               | 10-1 | <b>Mid-Term Exam 1.</b> Network theorems; source transformations, Tellegen's theorem. Superposition, reciprocity, voltage and current source substitution.                                     | Chapter 5 – 5.1 to 5.7 (review).              |
| 8                               | 10-8 | Thevenin's and Norton's theorems. Two-port networks; port definition and measurements; two-port immittance parameters.                                                                         | Chapter 17 – 17.3 to 17.4; handouts.          |

|    |       |                                                                                                                     |                                                           |
|----|-------|---------------------------------------------------------------------------------------------------------------------|-----------------------------------------------------------|
| 9  | 10-15 | Hybrid parameters; transmission parameters. Two-port models; properties of two-port networks.                       | Chapter 17 – 17.5 to 17.7.                                |
| 10 | 10-22 | Feedback amplifier analysis and design using two-port models.                                                       | Handouts and class notes.                                 |
| 11 | 10-29 | <b>Mid-Term Exam 2.</b> Review of op-amps; parameters and models.                                                   | Chapter 6 – 6.1 to 6.5;<br>Chapter 13 – 13.5.             |
| 12 | 11-5  | Active RC filters; definitions, applications, properties. SAB filters; Sallen and Key topology and designs.         | Chapter 16 – 16.1 to 16.3;<br>16.4 to 16.5; handouts.     |
| 13 | 11-12 | SAB filters; multiple-feedback topology and designs. Steady-state analysis and power; sinusoidal voltages; phasors. | Chapter 10 – 10.1 to 10.5.                                |
| 14 | 11-19 | Complex immittance; phasor diagrams. Electric power; instantaneous and average power.                               | Chapter 10 – 10.6 to 10.11;<br>Chapter 11 – 11.1 to 11.4. |
| 15 | 11-26 | Complex power; power factor; ideal transformers. Three-phase networks; three-phase voltage and current.             | Chapter 11 – 11.5 to 11.10.                               |
| 16 | 12-3  | $\Delta$ -to-Y transformation, Y-to- $\Delta$ transformation. Balanced and unbalanced three-phase networks.         | Chapter 12 – 12.1 to 12.6.                                |
| 17 | 12-10 | <b>Final Exam: Monday, December 10, 2010, 8:00 am to 10:30 am.</b>                                                  |                                                           |

#### References:

1. T.L. Floyd, Electric Circuits Fundamentals, 7<sup>th</sup> Edition, Pearson Education, Inc., UpperSaddle River, NJ, 07458, 2007.
2. J.D. Irwin and R.M. Nelms, Basic Engineering Circuit Analysis, 8<sup>th</sup> Edition, John Wiley and Sons, Inc., Hoboken, NJ, 07030, 2005.
3. R.T. Paynter and B.J.T. Boydell, Electronics Technology Fundamentals, 3<sup>rd</sup> Edition, Pearson Education, Inc., Upper Saddle River, NJ, 07458, 2009.
4. M.E. Van Valkenburg, Network Analysis, 3rd Edition, Prentice-Hall, Inc., Englewood Cliffs, NJ, 1974.
5. B. Friedland, O. Wing, and R. Ash, Principles of Linear Networks, McGraw-Hill Book Co., Inc., New York, NY, 1961.
6. P.M. DeRusso, R.J. Roy, and C.M. Close, State Variables for Engineers, John Wiley and Sons, Inc., New York, NY, 1967.
7. O. Wing, Circuit Theory With Computer Methods, Holt, Rinehart, and Winston, New York, NY, 1972.
8. L.O. Chua and P.M. Lin, Computer-Aided Analysis of Electronic Circuits, Prentice-Hall, Inc., Englewood Cliffs, NJ, 1975.
9. J. Vlach and K. Singhal, Computer Methods for Circuit Analysis and Design, Van Nostrand Reinhold Company, New York, NY, 1983.

**Student Learning Outcomes:**

| <b>EE 3446</b><br>Course Learning Objectives<br>and Assessment Approach |                                                                                                                                                                                             |              |                     |
|-------------------------------------------------------------------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|--------------|---------------------|
| Number                                                                  | Course Learning Objective (CLO)                                                                                                                                                             | ABET Outcome | Assessment Approach |
| 1                                                                       | An understanding of complex power calculations, power factor, the maximum power transfer theorem, coupled inductors, and ideal transformers.                                                | a, e         | exam problems       |
| 2                                                                       | An understanding of the basics of three-phase systems, Y and $\Delta$ connections, balanced and unbalanced systems, and power calculations and measurements.                                | a, e         | exam problems       |
| 3                                                                       | An understanding of frequency response in passive and active circuits, series and parallel resonance, and a working knowledge of Bode plots.                                                | a, e         | exam problems       |
| 4                                                                       | An understanding of Laplace transforms, relationships between time and frequency domains, network response functions, significance of network poles and zeros, and the convolution theorem. | a, e         | exam problems       |
| 5                                                                       | An ability to mathematically formulate multiple mesh and node analysis matrix equations for linear networks with and without dependent sources.                                             | a, c, e      | exam problems       |
| 6                                                                       | An understanding of network theorems such as Tellegen's, theorem, superposition, reciprocity, and Thevenin's and Norton's theorem.                                                          | a, e         | exam problems       |
| 7                                                                       | An understanding of operational amplifiers, and a working knowledge of first and second-order active RC filters.                                                                            | a, e         | exam problems       |
| 8                                                                       | An ability to analyze two-and three-port networks, and an understanding of two-port network applications.                                                                                   | a, e         | exam problems       |
| 9                                                                       | The application of computer tools and software in the solution of circuit design problems.                                                                                                  | a, k         | lab experiments     |
| 10                                                                      | An ability to use conventional electrical engineering instruments and equipment.                                                                                                            | a, e, k      | lab experiments     |
| 11                                                                      | A working understanding of important analytical principles.                                                                                                                                 | a, b, c      | lab experiments     |
| 12                                                                      | An ability to work as a member of a team.                                                                                                                                                   | d            | lab experiments     |
| 13                                                                      | An ability to understand and resolve professional ethical issues.                                                                                                                           | f            | lab experiments     |

Outcomes a-k  
(a-k as listed by ABET)

- a. an ability to apply knowledge of mathematics, science, and engineering;
- b. an ability to design and construct experiments, as well as to analyze and interpret data;
- c. an ability to design a system, component, or process to meet desired needs within realistic constraints such as economic, environmental, social, political, ethical, health and safety, manufacturability, and sustainability;
- d. an ability to function on multidisciplinary teams;
- e. an ability to identify, formulate, and solve engineering problems;
- f. an understanding of professional and ethical responsibility;
- g. an ability to communicate effectively;
- h. the broad education necessary to understand the impact of engineering solutions in a global and societal context;
- i. a recognition of the need for, and an ability to engage in lifelong learning;
- j. a knowledge of contemporary issues;
- k. an ability to use the techniques, skills, and modern engineering tools necessary for engineering practice.

**General Lecture Section Information:**

- 1. There are 45 class meetings scheduled for the 17 weeks of the Fall 2012 Semester. Forty-two of these meetings are devoted to in-class lectures, three devoted to examinations – two mid-terms and one final.
- 2. Your 100% attendance in lectures and labs is mandatory and required. This requirement will not be waived for any reason.
- 3. There will be five to six homework assignments, usually given on two-week intervals. Each assignment requires about five to ten hours for completion. These assignments will be graded and will be included as part of your total course grade. Past experience has shown that students who spend the necessary time to work these assignments usually do quite well on the examinations. Solutions to homework problems will be e-mailed to all students by the end of the day the assignment is due. Homework turned in after the solutions have been sent out will not be counted and will receive a grade of zero.
- 4. Mid-term examinations will be given on the 7<sup>th</sup> and 11<sup>th</sup> weeks, and will be one hour written exercises.
- 5. The final examination will be given on **Monday December 10, 2012** and will be a comprehensive written examination. **NOTICE:** The UTA registrar has demanded a firm, unforgivable final date (four calendar days after the last final examination date) for the submission of course grades from all instructors. Because of this rigid schedule, the date planned for the final examination (**Monday December 10, 2012 8:00 am to 10:30 am**) is fixed and will not be changed for any reason. Therefore, all students, without exception, must take the final examination at this time.
- 6. You are respectively requested to turn off any and all communication devices while in the Seminar and the Lecture/Lab Sections. Communication devices of any type are not allowed to be used on any and all exams. No exceptions. Calculators without communication capability are permitted.
- 7. All examinations will be pencil, paper, and calculator exercises. All students will take these exams at the scheduled times in the classroom.
- 8. There will be absolutely no late or make-up mid-term examinations given unless a written request has been submitted to and approved by the instructor at least two weeks prior to the examination date. As a rule, make-up examinations are several orders of magnitude more difficult than examinations given on the scheduled dates. Please be advised that illness or any other absence on the examination date does not constitute a valid reason for a make-up examination.
- 9. The *total grade* (T) for this course is based upon homework, lab, two mid-term exams, and final exam grades. The total T is computed from

$$T = 0.05 \cdot HW + 0.15 \cdot LA + 0.25 \cdot (MTE1 + MTE2) + 0.3 \cdot FE$$

HW = homework average

LA = lab grade

MTE1 = mid-term exam 1 grade

MTE2 = mid-term exam 2 grade

FE = final exam grade

(1)

10. Failure to take (either scheduled or approved make-up) any or all exams (mid-terms and final) will automatically result in a letter grade of 'F' for the course. You are required to take all exams.
11. Office hours are posted outside my office (NH526). If you have any questions and/or adverse difficulty with the lectures or class material, I strongly suggest that you call or e-mail me (during regular working hours, of course). If necessary, a scheduled office visit can be arranged.
12. Additional important dates:
  - Census date – **Monday, September 10, 2012.**
  - Last day to drop classes – **Wednesday, October 31, 2012.**
  - Spring 2013 registration starts – **Monday, November 5, 2012.**
  - Thanksgiving Holidays – **Thursday, November 22, 2012 and Friday, November 23, 2012.**
  - Last day of classes – **Wednesday, December 5, 2012.**

### **Drop Policy:**

Please refer to the University policy for dropping courses.

### **Americans With Disabilities Act:**

The University of Texas at Arlington is on record as being committed to both the spirit and letter of federal equal opportunity legislation; reference Public Law 92-112 - The Rehabilitation Act of 1973 as amended. With the passage of federal legislation entitled *Americans with Disabilities Act (ADA)*, pursuant to section 504 of the Rehabilitation Act, there is renewed focus on providing this population with the same opportunities enjoyed by all citizens. As a faculty member, I am required by law to provide "reasonable accommodations" to students with disabilities, so as not to discriminate on the basis of that disability. Student responsibility primarily rests with informing faculty of their need for accommodation and in providing authorized documentation through designated administrative channels. Information regarding specific diagnostic criteria and policies for obtaining academic accommodations can be found at [www.uta.edu/disability](http://www.uta.edu/disability). Also, you may visit the Office for Students with Disabilities in room 102 of University Hall or call them at (817) 272-3364.

### **Student Support Services Available:**

The University of Texas at Arlington supports a variety of student success programs to help you connect with the University and achieve academic success. These programs include learning assistance, developmental education, advising and mentoring, admission and transition, and federally funded programs. Students requiring assistance academically, personally, or socially should contact the Office of Student Success Programs at 817-272-6107 for more information and appropriate referrals.

### **Final Review Week:**

A period of five class days prior to the first day of final examinations in the long sessions shall be designated as Final Review Week. The purpose of this week is to allow students sufficient time to prepare for final examinations. During this week, there shall be no scheduled activities such as required field trips or performances; and no instructor shall assign any themes, research problems or exercises of similar scope that have a completion date during or following this week unless specified in the class syllabi. During Final Review Week, an instructor shall not give any examinations constituting 10% or more of the final grade, except makeup tests and laboratory examinations. In addition, no instructor shall give any portion of the final examination during Final Review Week. Classes are held as scheduled during this week and lectures and presentations may be given.

### **E-Culture Policy:**

The University of Texas at Arlington has adopted the University email address as an official means of communication with students. Through the use of email, UT-Arlington is able to provide students with relevant and timely information, designed to facilitate student success. In particular, important information concerning registration, financial aid, payment of bills, and graduation may be sent to students through email. All students are assigned an email account and information about activating and using it is available at [www.uta.edu/email](http://www.uta.edu/email). New students (first semester at UTA) are able to activate their email account 24 hours after registering for courses. There is no additional charge to students for using this account, and it remains active as long as a student is enrolled at UT-Arlington. Students are responsible for checking their email regularly.

**Ethics:****Student Responsibility**

Undergraduate and graduate students assume full responsibility for knowledge of all University rules, regulations and deadlines published in the Undergraduate and Graduate Catalogs and of all departmental and program requirements concerning their degree programs.

**Academic Dishonesty**

All students are expected to pursue their academic careers with honesty and integrity. Academic dishonesty includes, but is not limited to, cheating on a test or other coursework, plagiarism (offering the work of another as one's own) and unauthorized collaboration with another person. Students found responsible for dishonesty in their academic pursuits are subject to penalties that may range from disciplinary probation, suspension or expulsion from the University. In accordance with the Rules and Regulations of the Board of Regents of The University of Texas System (Part One, Chapter VI), institutional procedures regarding allegations of academic dishonesty are outlined in Part Two, Chapter 2, of the U.T. Arlington Handbook of Operating Procedures. This information may be obtained by accessing the Dean of Students' Web site at [www.uta.edu/studentaffairs/dos](http://www.uta.edu/studentaffairs/dos) or the Student Judicial Affairs' Web site at [www.uta.edu/studentaffairs/judicialaffairs](http://www.uta.edu/studentaffairs/judicialaffairs). Copies of each regulation can be obtained in the Dean of Students' Office on the lower level of the University Center.

**Definitions (UTA Handbook of Operating Procedures)**

F. scholastic dishonesty, including, but not limited to, cheating on an examination or an assignment, plagiarism, and collusion;

1. *cheating on an examination or an assignment* includes:
  - a. copying the work of another, engaging in written, oral or any other means of communication with another, or giving aid to or seeking aid from another when not permitted by the instructor;
  - b. using material during an examination or when completing an assignment that is not authorized by the person giving the examination or making the work assignment;
  - c. taking or attempting to take an examination for another, or allowing another to take or attempt to take an examination for a student;
  - d. using, obtaining, or attempting to obtain by any means, the whole or any part of an un-administered examination or work assignment;
  - e. any act designed to give unfair advantage to a student or the attempt to commit such an act;
2. *plagiarism* means the unacknowledged incorporation of the work of another in work that is offered for credit;
3. *collusion* means the unauthorized collaboration with another in preparing work that is offered for credit.



The following is an excerpt from the College of Engineering's statement on Ethics, Professionalism, and Conduct of Engineering Students. Read the statement carefully, sign it, and return it to your instructor. You may make a copy for your records. Additional copies of this statement can be obtained from your instructor or the Office of the Dean of Engineering.

**STATEMENT ON ETHICS, PROFESSIONALISM, AND CONDUCT  
FOR ENGINEERING STUDENTS  
COLLEGE OF ENGINEERING  
THE UNIVERSITY OF TEXAS AT ARLINGTON**

The College cannot and will not tolerate any form of academic dishonesty by its students. This includes, but is not limited to cheating on examination, plagiarism, or collusion.

**Cheating** on an examination includes:

1. Copying from another's paper, any means of communication with another during examination, giving aid to or receiving aid from another during examination;
2. Using any material during examination that is unauthorized by the proctor;
3. Taking or attempting to take an examination for another student or allowing another student to take or attempt to take an examination for oneself.
4. Using, obtaining, or attempting to obtain by any means the whole or any part of an un-administered examination.

**Plagiarism** is the unacknowledged incorporation of another's work into work which the student offers for credit.

**Collusion** is the unauthorized collaboration of another in preparing work that a student offers for credit.

I have read and I understand the above statement.

In addition, I understand that, in order to ensure fairness to all students, exams will be proctored and possibly videotaped.

Course and section number: \_\_\_\_\_EE 3446-001/101/102\_\_\_\_\_

Date: \_\_\_\_\_

Student's signature: \_\_\_\_\_

Student's name, printed: \_\_\_\_\_

Student's ID number: \_\_\_\_\_

Student's e-mail address: \_\_\_\_\_  
(please print clearly)